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IN THE DRAWINGS:

Please insert the attached Replacement Sheet(s) in this Application.

Address	L	ock bit	High byte		Low byte
QID	0	RmW	Blocks to remove		Lock control
QID + 2			Block size (12-bits)		
QID+4		-	Tail of blocks		
QID + 6	I	₹mW	Used blocks		Empty blocks
					(/Used Threshold)
QID + 8		1	Type (Single = -1/Element = RID)		
QID + 10		1	Committed Tail Pointer		
QID + 12		1	Committed Tail Offset(12-bits)		
Q1D + 14		2	Current Head Pointer		
	1		or		
			Current Tail		ail Pointer
QID + 16		2	Current Head Offset		
	1		or		
			Current Tail Offset(12-bits)		
QID + 18		2	Committed Head Pointer		
QID + 20		2	- Committed Head Offset(12-bits)		
QID + 22		2			Blocks to release

Figure 2

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REMARKS

Favorable reconsideration of this application is requested in view of the foregoing amendments and the following remarks. Claims 1-7 are pending in the application. Claims 6-7 are newly presented.

On April 27, 2005 a telephone interview was held with the Examiner regarding this application. The courteousness extended by the Examiner is very much appreciated. This reply sets forth in written form many of the comments presented during the interview.

The claims are amended in order to more clearly define the invention, support for which is found at pages 14-15 of the specification as originally filed. Support for replacing the phrase latest read pointer with the phrase current head pointer is found at page 14, lines 28-29; page 15, lines 2, 4, 6-7, 12 and 26-27; and page 16, line 6. Support for replacing the phrase committed read pointer with the phrase committed head pointer is found at page 14, lines 28-29; and page 15, lines 5-6, 26-27 and 29. Claim 2 is rewritten in Independent form. Claims 3 and 4 are amended to avoid multiple dependency. Support for replacing the phase latest write pointer in claim 5 with the phrase current tail pointer is found at page 14, lines 2-3 and 5-6. Support for replacing the phase committed write pointer in claim 5 with the phrase committed tail pointer is found at page 14, lines 4-6 and 11-12. Support for new claims 6 and 7 is found in claims 3 and 4, respectively.

At page 2 of the Action, the Examiner objects to the drawings not depicting the claimed limitations of second latest read pointer, latest write pointer, and committed write pointer. Applicant submits herewith a proposed figure 2 that overcomes the objection to the drawings as set forth by the Examiner. Support for Figure 2 is found in table 2 at page 9, at page 14 last paragraph to page 15 third paragraph and in the claims as originally filed.

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The phrase second latest read pointer was recited in claim 4 as originally filed and is now replaced in claims 4 and 7 with the phrase second latest head pointer. Claims 4 and 7 are directed to embodiments of the invention that include repeating the reading, updating, transferring and conditional updating steps of claims 1 and 2, respectively. Therefore, the second latest head pointer is merely another latest head pointer and a latest head pointer is depicted in figure 2.

The phrase latest write pointer is recited in claim 5 as originally filed and is now replaced in claim 5 with the phrase current tail pointer. Figure 2 depicts a current tail pointer. Support for depicting the current tail pointer in figure 2 is found at page 14, lines 2-3 and 5-6.

The phrase committed write pointer was recited in claim 5 as originally filed and is now replaced in claim 5 with the phrase committed tail pointer. Figure 2 depicts a committed tail pointer. Support for depicting the committed tail pointer in figure 2 is found at page 14, lines 4-6 and 11-12. Incidentally, support for depicting a current tail offset in figure 2 is found at page 14, line 15.

The specification was previously amended to recite a brief description of figure 2.

Applicant requests that the Examiner approve figure 2 and withdraw the objection to the drawings. An annotated marked-up version of figure 2 is attached hereto. The annotations are with regard to the previous version of figure 2.

Accordingly, withdrawal of this objection is respectfully requested.

Claims 1-5 stand rejected under 35 USC 102(b) as anticipated by Hamstra (i.e., U.S. Pat. No. 5,016,221). With regard to the claims as amended, this rejection is untenable.

Hamstra's commit flag indicates the end of what is to be sent. Hamstra's read flag indicates the end of what has been sent. Although Hamstra mentions the possibility of repetitive sending, such a Hamstra broadcast would be a simple beacon because nothing in Hamstra describes or suggests the claimed step of receiving confirmation.

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Hamstra fails to disclose a method of managing data stored in a queue wherein data is read from a queue and a current head pointer is placed at the location corresponding to the end of the read data, the data is then transferred to a destination, and then, after receiving confirmation that the data transfer is successful, a committed head pointer is updated to a location in memory corresponding to the end of the data. With the invention of claim 1, therefore, the claimed "current head pointer" always moves ahead of the "committed head pointer". As set out in the summary of the invention portion of the description of this application as originally filed, such an operation allows uncommitted data to be stored (for example for retransmission if required) without the provision of a separate area of memory.

Nowhere within Hamstra is such an arrangement comprising a committed head pointer following a current head pointer disclosed. Instead, referring to Figures 3A to 3E of Hamstra, a read pointer 30, commit pointer 32, and write pointer 34 are provided for use with a FIFO RAM. As shown in Figure 3A of Hamstra, when the RAM is empty, the three pointers are all set at the same memory location (cf. Column 7 lines 13-15 of Hamstra). When information has begun to be received and stored within the RAM of Hamstra, the write pointer 34 is advanced, leaving a block 50 of stored, but as yet uncommitted, binary information within the RAM (cf. Column 7 lines 17 to 23, and Figure 3B of Hamstra). If it is decided that the stored block of data 50 is to be retained for use, the commit pointer 32 is advanced to coincide with the write pointer 34 (cf. Figure 3C, and column 7 lines 31 to 35). Committed data 50 between the read pointer 30 and the commit pointer 32 of Hamstra is now available to be read out from the RAM (cf. Column 7 lines 35-37). Such a read operation is illustrated in Figure 3E of Hamstra, wherein, as the committed binary information 50 is read out, the read pointer 30 advances. The read pointer 30 of Hamstra may advance up to, but no further than, the commit pointer 32 (cf. Column 8 lines 50 to 53).

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Moreover, the above arguments also apply to the transmission interface 11 shown in Figure 4 of Hamstra. In particular, transmission of outgoing binary information from such a transmission interface 11 is controlled by the pointer controller 20. More specifically, and as described above for Figure 3E of Hamstra, the read pointer 30 is advanced as committed binary information 50 committed for transmission is read out of the RAM (cf. Column 10 lines 63-67). Thus, it is clear from Hamstra itself that transmission from the transmission interface 11 by reading data from the RAM 14 thereof, is the same as reading data from the RAM of the reception interface 10 of Figure 1. In neither case is a "committed head pointer" which follows a current head pointer as required by independent claim 1 disclosed or suggested by Hamstra.

Accordingly, withdrawal of this rejection is respectfully requested.

Other than as explicitly set forth above, this reply does not include acquiescence to statements in the Office Action. In view of the above, all the claims are considered patentable and allowance of all the claims is respectfully requested. The Examiner is invited to telephone the undersigned (at direct line 512-394-0118) for prompt action in the event any issues remain that prevent the allowance of any pending claims.

In accordance with 37 CFR 1.136(a) pertaining to patent application processing fees,
Applicant requests an extension of time from April 28, 2005 to May 28, 2005 in which to respond
to the Office Action dated January 28, 2005. A notification of extension of time is filed herewith.

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The Director of the U.S. Patent and Trademark Office is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 50-3204 of John Bruckner PC.

Respectfully submitted,

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Dated: May 31, 2005

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